

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: D. HISAMOTO, et al.
Application No.: Not Assigned Yet
Filed: On Even Date Herewith
For: FABRICATION METHOD AND STRUCTURE OF
SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE
Art Unit: Not Assigned Yet
Examiner: Not Assigned Yet

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR '1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 4, 2003

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of the documents listed in the attached form equivalent to form PTO/SB/08A for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

Each of the documents listed on the attached form equivalent to Form PTO/SB/08A is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.


Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry,

843.43311X00

Stout & Kraus, LLP Deposit Account No. 01-2135 (Docket No. 843.43311X00)
please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By 
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number	Not Assigned Yet	
			Filing Date	On Even Date Herewith	
			First Named Inventor	D. HISAMOTO, et al.	
			Art Unit	Not Assigned Yet	
Sheet	1	of	1	Examiner Name	Not Assigned Yet
				Attorney Docket Number	843.43311X00

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		5,969,383		10-1999	Chang et al.	

FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)					

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		"Physics of Semiconductor Devices" second edition by S. Sze, published by Wiley-Interscience publication (USA) in 1981, 8.6 Nonvolatile Memory Devices, pp 496-507	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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